

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1	"5642295".pn.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 14:40
L4	0	"5642295"".uref.."	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 14:40
L5	8	"5642295".uref.	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 15:20
L6	4216	chip near3 flash	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 15:20
L7	250	cpu with 6	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 15:21
L8	1563196	(eras\$3 or program\$4 or writ\$3)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 15:21
L9	234	7 and 8	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 15:21
L10	175	interrupt\$3 and 9	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:06
L11	898929	controller	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:06
L12	171	10 and 11	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:08
L13	136143	microcomputer	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:08
L14	44753	controller and 13	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:08
L15	14498	cpu and 14	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:08
L16	20191	13 with controller	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:08
L17	2989	cpu with 16	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:09

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L18	2386	(rom or eprom or eeprom or prom) and 17	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:09
L19	1417	((rom or eprom or eeprom or prom or flash)near3 (load\$3 or stor\$3 or writ\$5 or eras\$3)) and 17	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:12
L20	621	(interrupt\$3 or exception\$1) and 19	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:11
L21	2362	((rom or eprom or eeprom or prom or flash)near3 (load\$3 or stor\$3 or writ\$5 or eras\$3)) with interrupt\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:25
L22	65	20 and 21	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:14
L23	34	20 and boot\$1	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:14
L24	51	20 and boot\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:14
L25	363	((rom or eprom or eeprom or prom or flash)near3 (programm\$4 or eras\$3)) with interrupt\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:27
L26	120	boot\$3 and 25	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:27
L27	95	26 and chip	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:39
L28	2547	chip adj (eprom or rom or eeprom)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:39
L29	3468	chip adj (eprom or rom or eeprom or flash)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:39
L30	357	(program\$4 or eras\$3) near3 29	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:40
L31	231	interrupt\$3 and 30	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:41
L32	200	31 and cpu	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/04/20 16:41